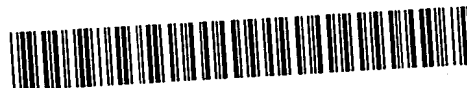


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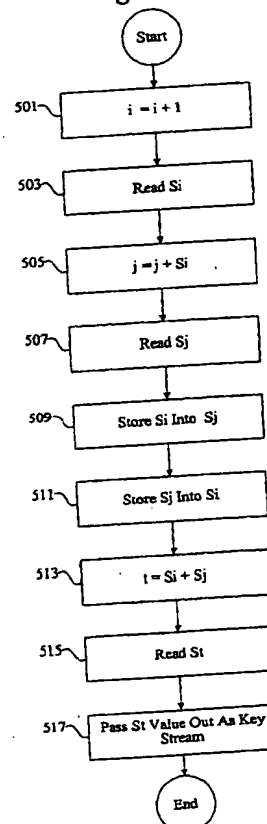
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(54) **Methods and apparatus for accelerating ARC4 processing**

(57) Methods and apparatus are provided for improving ARC4 processing in a cryptography engine. A multiple ported memory can be used to allow pipelined read and write access to values in memory. Coherency checking can be applied to provide that read-after-write and write-after-write consistency is maintained. Initialization of the memory can be improved with a reset feature occurring in a single cycle. Key shuffle and key stream generation can also be performed using a single core.

Fig. 5**EP 1 289 188 A3**



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EUROPEAN SEARCH REPORT

Application Number
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Y		1,2,5-9, 11,14 3,4,10, 12,15, 16,21-28	
A	* abstract *		
	* page 398, left-hand column, line 24 - line 34 *		
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<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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